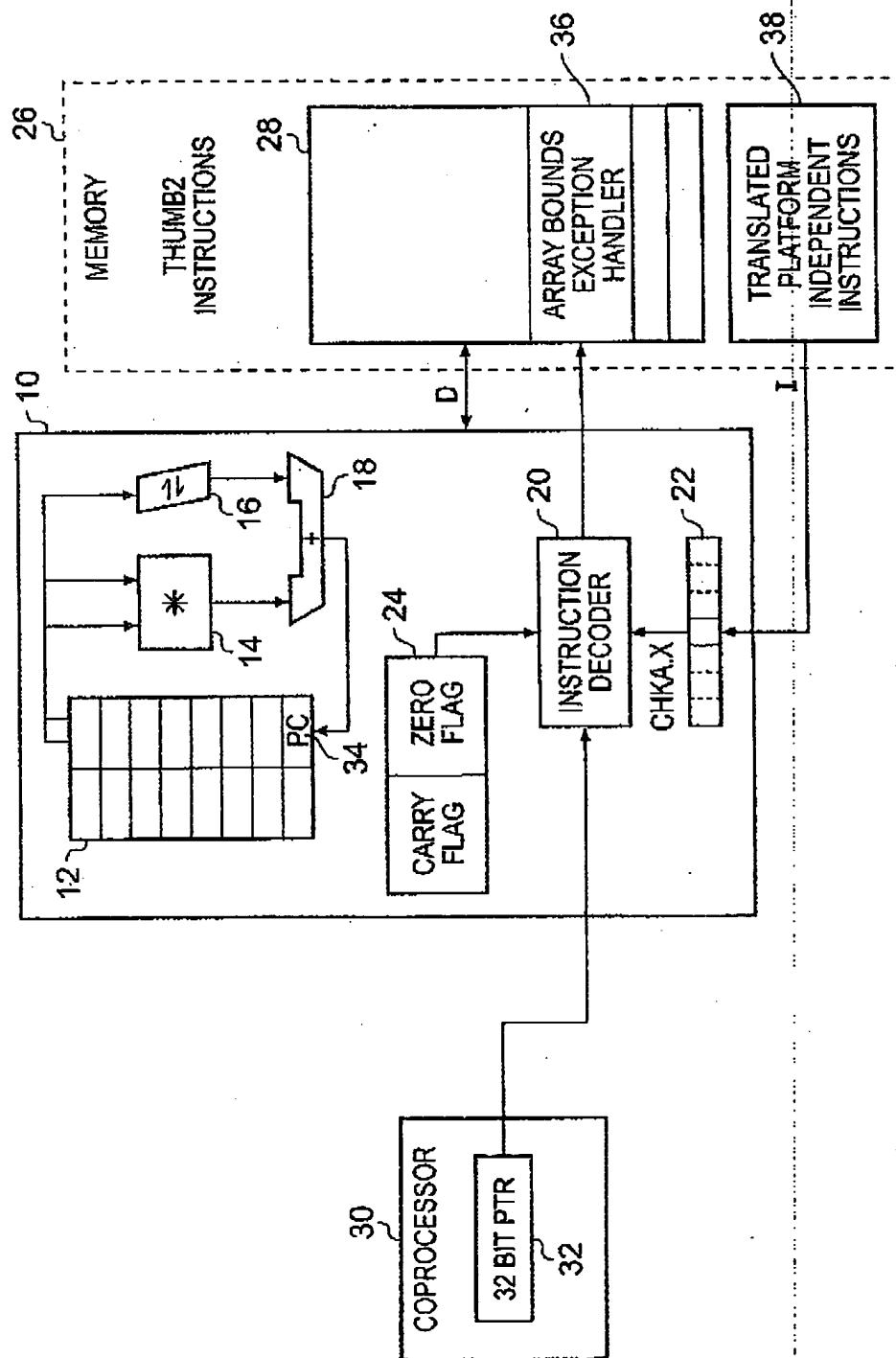


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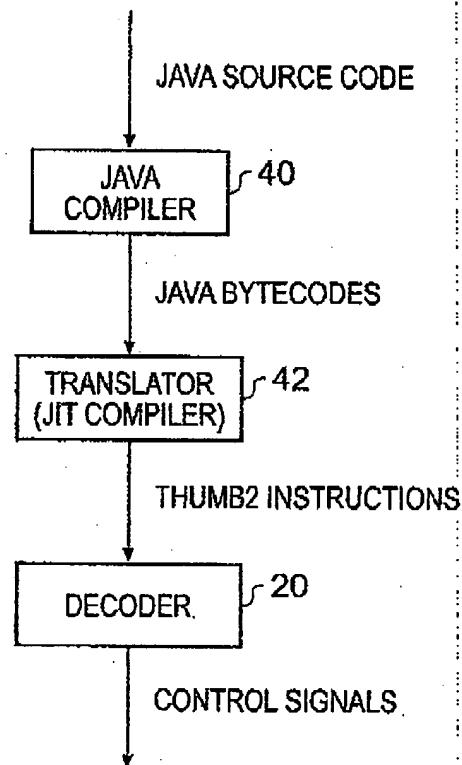


FIG. 2

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Appl. No. 10/807,999
Atty. Dkt. 550-540
Amtd. dated March 4, 2008
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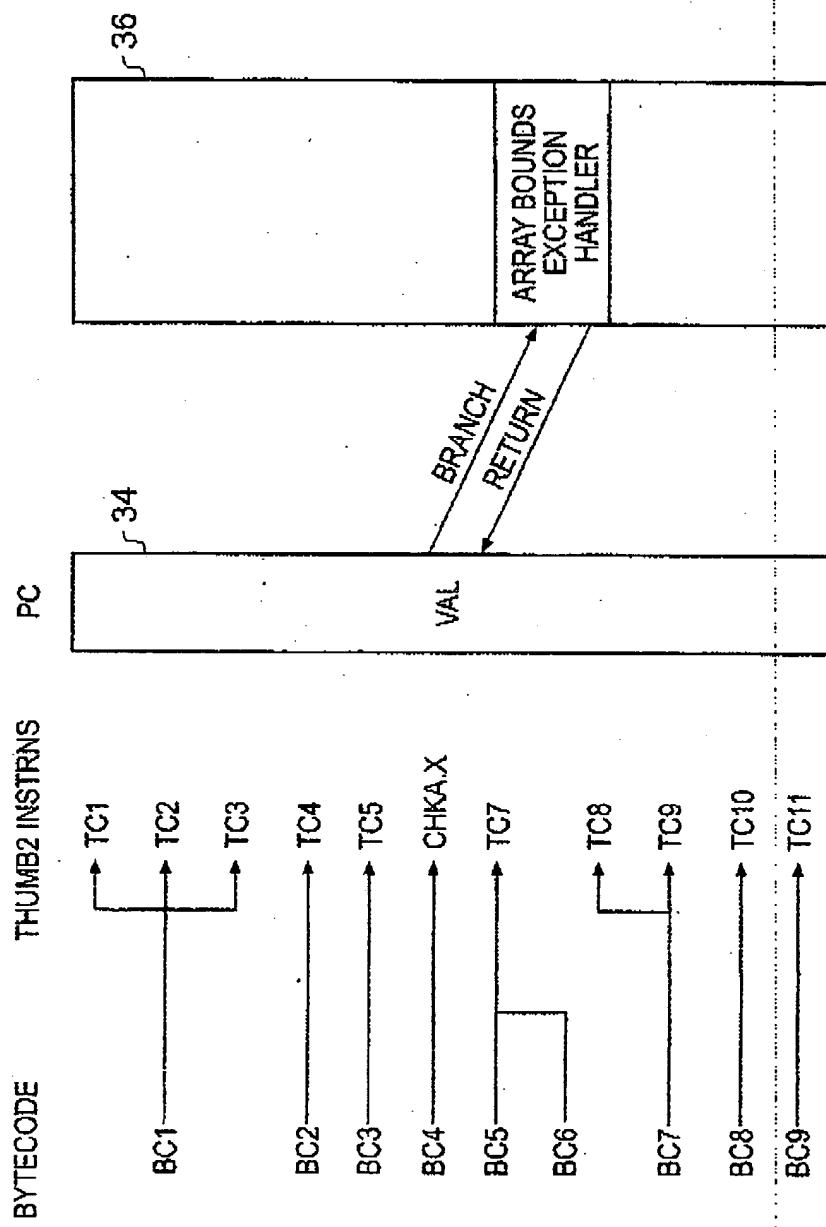


FIG. 3

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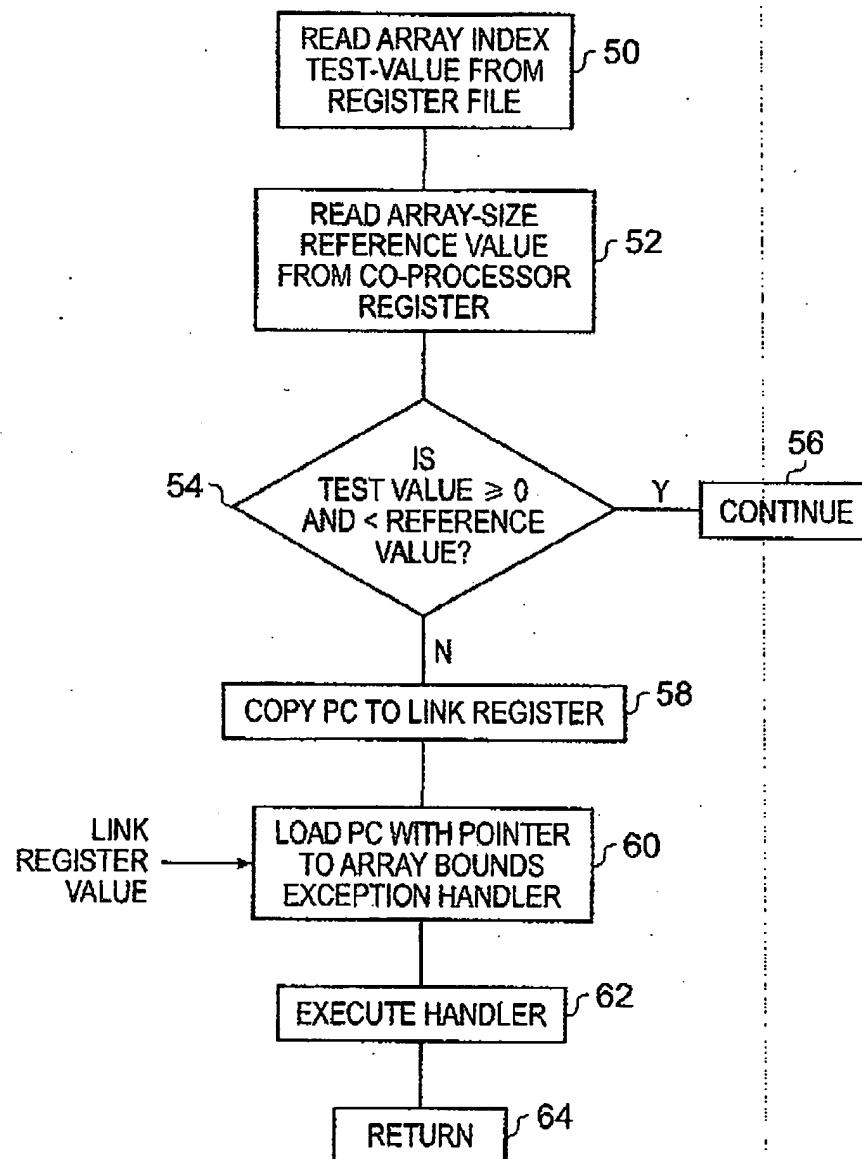


FIG. 4

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Instruction	CHKA.X	Rn, Rm (16-bit)
Encoding	15 14 13 12 11 10 9 8 7 6 5 3 2 0	
	< opcode	>
Thumb-2 Equivalent	CMP Rn, Rm MOV LS lr, pc ADD LS pc, HandlerBase, #8	H1 H2 Rm Rn
Definition	IF (unsigned) Rm >= (unsigned) Rn lr = pc pc, HandlerBase, #8, IndexException	
Encoding space	2^8	8 bits
Note	This is based upon the CMP(3) 16-bit Thumb-2 instruction that can use high registers	
Note	H1 contains the most significant bit for Rn, H2 the most significant bit for Rm	
Note	The LS case should almost never occur, so can be treated as exceptional behaviour	
Note	This instruction does not set condition flags	
Note	This comparison is UNSIGNED	
Note	Return stack prediction will not be required when the MOV lr,pc step is executed.	

FIG. 5